晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-800480SIMQW-A0
APPROVED BY	
DATE	

☐ Preliminary Specification
☑ Formal Specification

AMPIRE CO., LTD.
4F., No.116, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City221, Taiwan (R.O.C.)
新北市汐止區新台五路一段 **116** 號 **4** 樓(東方科學園區 **A** 棟)
TEL:886-2-26967269 , FAX:886-2-26967196 or 26967270

Approved by	Checked by	Organized by
Patrick	Mark	Tank

*This specification is subject to change without notice.

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2021/09/17	--	New Release	Tank

1. INTRODUCTION

Ampire Display Module is a color active matrix TFT-LCD that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD panel, timing controller. This TFT-LCD has a high resolution (800(R.G.B) x 480) and can display up to 262,144 colors.

1-1. Features

- 7” WVGA (16:9 diagonal) configuration
- Input interface voltage: 3.3V
- Data enable mode

1-2. Applications

- Portable TV
- Car user DVD
- Industrial application
- HMI (Human machine interface)

2. PHYSICAL SPECIFICATIONS

Item	Specifications	Unit
Display Resolution(dot)	800RGB (W) x 480(H)	dots
Active Area	152.4 (W) x 91.44 (H)	mm
Pixel Pitch	0.1905 (W) x 0.1905 (H)	mm
Color Configuration	R.G.B Vertical stripe	
LCD Type	Transmissive, Normally White	
Overall Dimension	165.0(W) x 104.44(H) x 7.96(D)	mm
View Direction	6 O’clock	
Brightness	350	cd/m^2
Contrast Ratio	400 : 1	
Backlight Unit	LED	
Display Color	262,144	colors
Interface	LVDS	

3. ABSOLUTE MAX. RATINGS

ITEM	SYMBOL	MIN	MAX	UNIT
Power Supply Voltage for LCD	V_{CC}	-0.5	4.0	V
Signal input voltage		-0.5	V_{CC}+0.3	V
Operation Temperature	Top	-20	70	℃
Storage Temperature	Tstg	-30	80	℃

The following values are maximum operation conditions, If exceeded, it may cause faulty operation or damage

4. ELECTRICAL CHARACTERISTICS

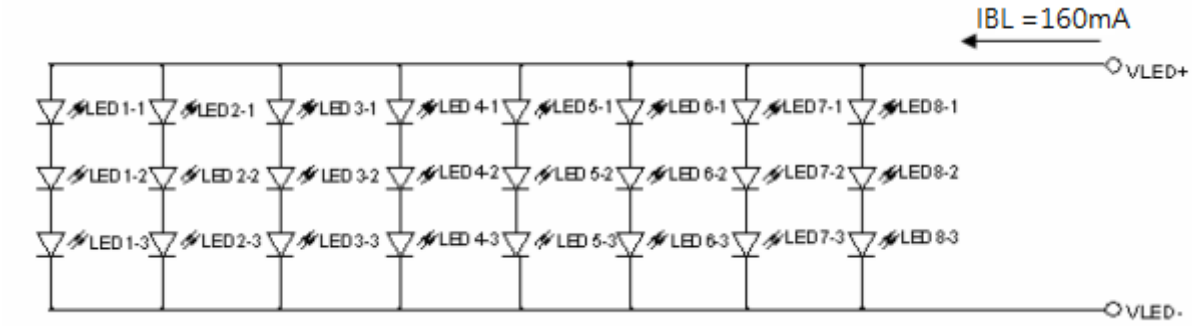
4-1 TFT LCD Module voltage

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
Power Supply Voltage For LCD		V_{CC}	3.0	3.3	3.6	V	
Power Supply Current For LCD		I_{CC}	-	200	260	mA	V_{CC}=3.3V
Logic Input Voltage	Input Voltage	V_{IN}	0	-	V_{CC}	V	
	High Level input Voltage	V_{IH}	2	-	V_{CC}	V	
	Low Level input Voltage	V_{IL}	0	-	0.8	V	

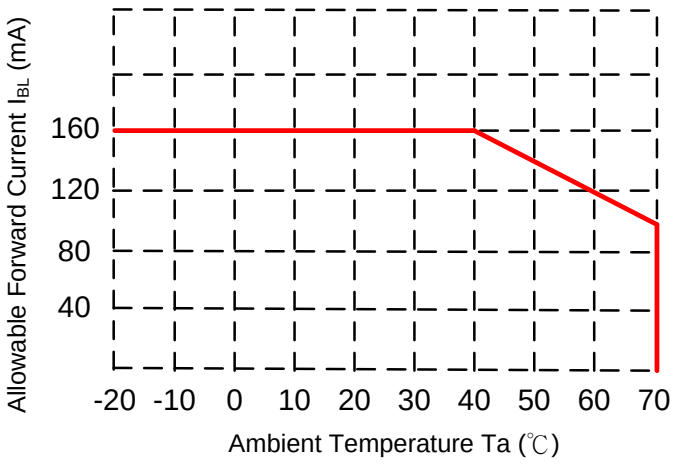
4-2 LED Driving Conditions

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
LED Backlight Voltage	V_{BL}	-	9.9	-	V	Note1
LED Backlight Current	I_{BL}	-	160	-	mA	
LED Life Time	-	10K	20K		Hr	Note2

Note 1 : There are 8 Groups LED shown as below , V_{BL} =9.9V , I_{BL} =160mA.



Note 2 : Brightness to be decreased to 50% of the initial value.



5. INTERFACE

Pin No.	Symbol	Function
1	VDD	POWER SUPPLY:3.3V
2	VDD	POWER SUPPLY:3.3V
3	GND	Power Ground
4	GND	Power Ground
5	IN0-	Transmission Data of Pixels 0
6	IN0+	Transmission Data of Pixels 0
7	GND	Power Ground
8	IN1-	Transmission Data of Pixels 1
9	IN1+	Transmission Data of Pixels 1
10	GND	Power Ground
11	IN2-	Transmission Data of Pixels 2
12	IN2+	Transmission Data of Pixels 2
13	GND	Power Ground
14	CLK-	Sampling Clock
15	CLK+	Sampling Clock
16	GND	Power Ground
17	LEDADJ	LED Dimming Pin; **(Keep this pin NC in this model).**
18	VLED	LED Driver IC Power Supply 3.3V~5.0V; **(Keep this pin NC in this model).**
19	GND	Power Ground
20	GND	Power Ground

Note: The LCM AM-800480STMQW-A0 does not build-in LED driver circuit. Please keep pin 17 and 18 NC.

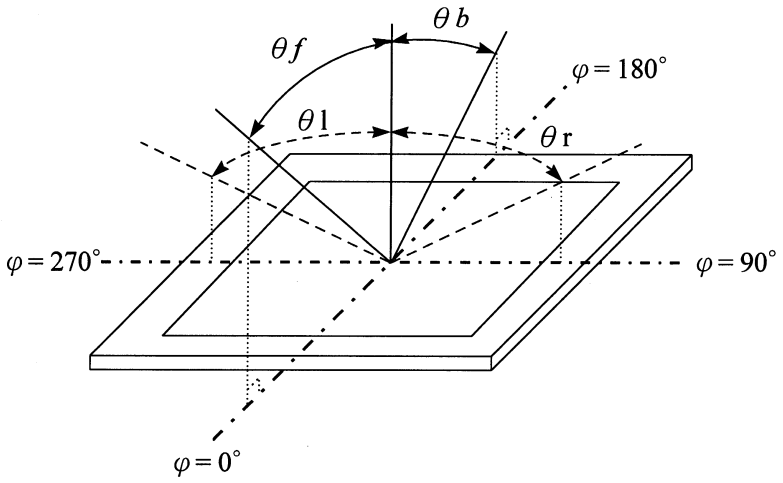
Connector Part No.: DF19G-20P-1H / P1.0 20PIN or equivalent (Female)
Mating connector Part No.: DF19 -20S-1C / P1.0 20PIN or equivalent (Male)

6. OPTICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle	Front	θ f	CR≧10	50	60	--	deg.	(1)(2)(3)
	Back	θ b		60	70	--		
	Left	θ l		60	70	--		
	Right	θ r		60	70	--		
Contrast Ratio		CR	Θ=Φ=0°	250	400	--	--	(1)(3)
Response Time		T_r	Θ=Φ=0°	--	5	10	ms	(1)(4)
		T_f		--	11	16	ms	(1)(4)
Color Chromaticity	White	Wx		0.249	0.299	0.349	--	(1)
		Wy		0.278	0.328	0.378		
Luminance		L	Θ=Φ=0°	300	350	--	cd/m^2	(1)(5)
Luminance Uniformity		ΔL	Θ=Φ=0°	70	--	--	%	(1)(5)(6)

Note 1: Ta=25°C. To be measured on the center area of panel after 10 minutes operation.

Note 2: Definition of Viewing Angle



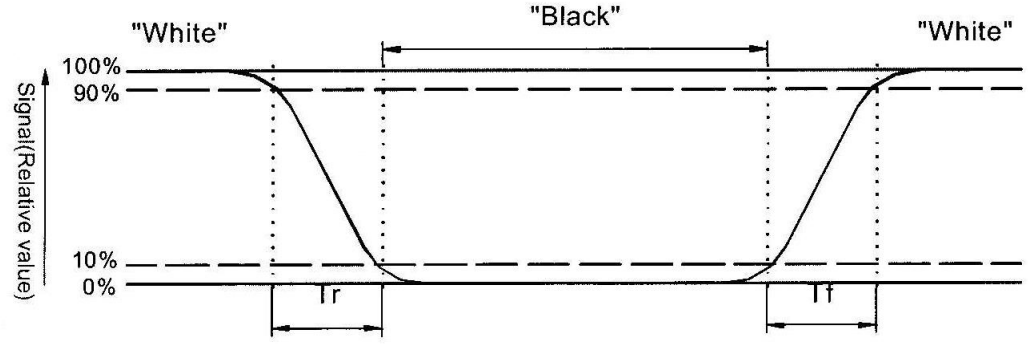
Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

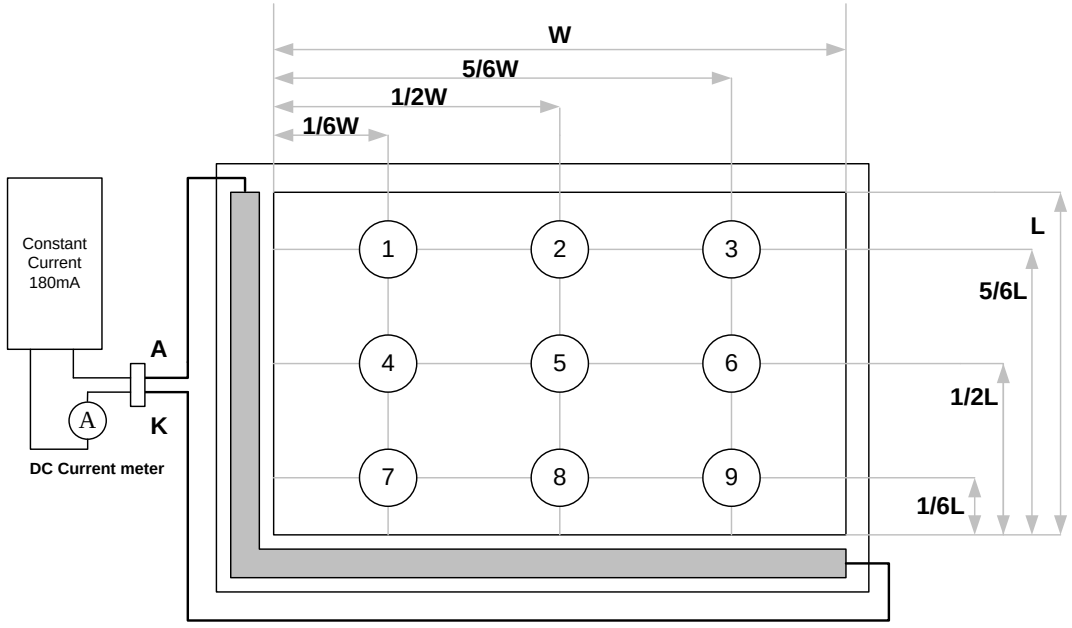
$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector Output when LCD is at "Black" state}}$$

Note 4: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black“ to “white”(falling time) and from ”white” to “black” (rising time) respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



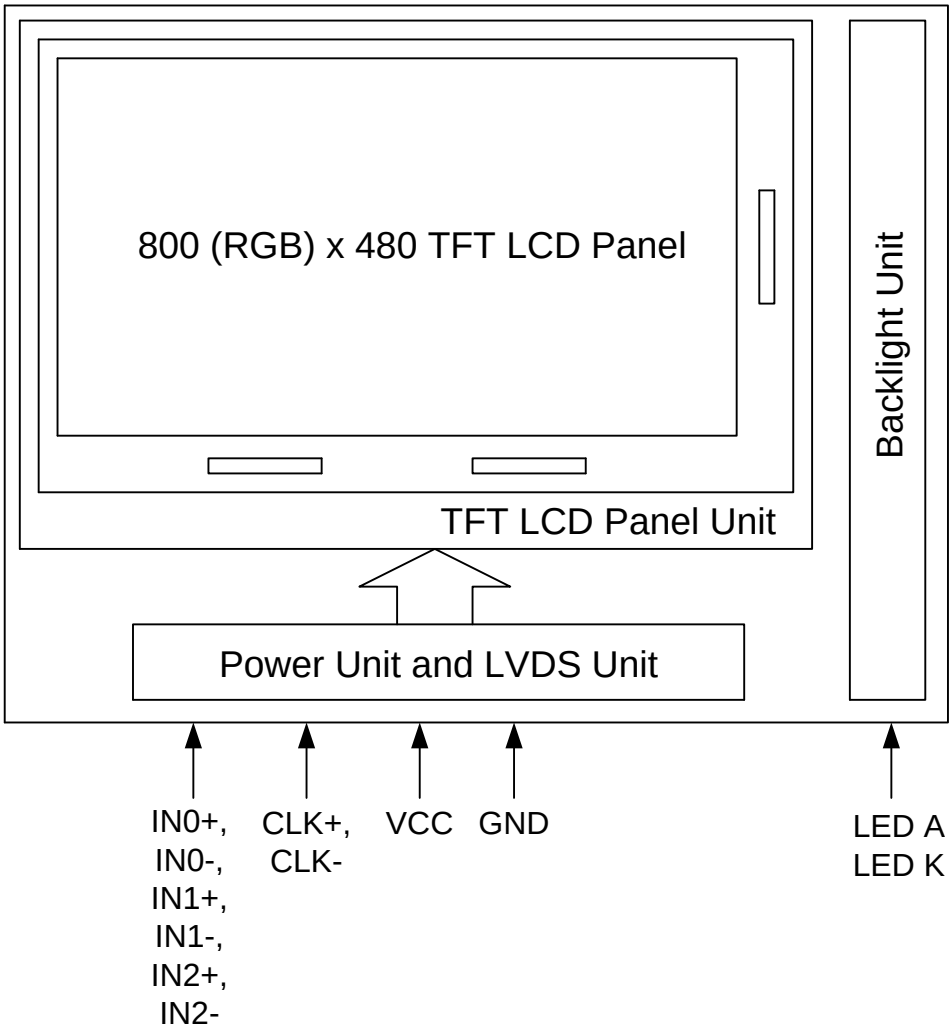
Note 5: Luminance is measured at point 5 of the display.



Note 6: Definition of Luminance Uniformity

ΔL = [L(min.) of 9 points / L(max.) of 9 points] X 100%

7. Block Diagram



8. INPUT SIGNAL

8-1 LVDS Signal

switching characteristics over recommended operating conditions (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP†	MAX	UNIT
t_{su}	Setup time, D0–D20 to CLKOUT↓	C_L = 8 pF, See Figure 5	5			ns
t_h	Data hold time, CLKOUT↓ to D0–D20		5			ns
$t_{(RSKM)}$	Receiver input skew margin§ (see Figure 7)	t_c = 15.38 ns (±0.2%), |Input clock jitter| < 50 ps¶,	550	700		ps
t_d	Delay time, CLKIN↑ to CLKOUT↓ (see Figure 7)	V_{CC} = 3.3 V, t_c = 15.38 ns (±0.2%), T_A = 25°C	3	5	7	ns
t_{en}	Enable time, $\overline{SHTDN}$ to phase lock	See Figure 7		1		ms
t_{dis}	Disable time, $\overline{SHTDN}$ to off state	See Figure 8		400		ns
t_t	Transition time, output (10% to 90% t_r or t_f) (data only)	C_L = 8 pF		3		ns
t_t	Transition time, output (10% to 90% t_r or t_f) (clock only)	C_L = 8 pF		1.5		ns
t_w	Pulse duration, output clock			0.50 t_c		ns

† All typical values are at V_{CC} = 3.3 V, T_A = 25°C.
§ The parameter $t_{(RSKM)}$ is the timing margin available to allocate to the transmitter and interconnection skews and clock jitter. The value of this parameter at clock periods other than 15.38 ns can be calculated from t_{RSKM} = tc/14 – 550 ps.
¶ |Input clock jitter| is the magnitude of the change in input clock period.

PARAMETER MEASUREMENT INFORMATION

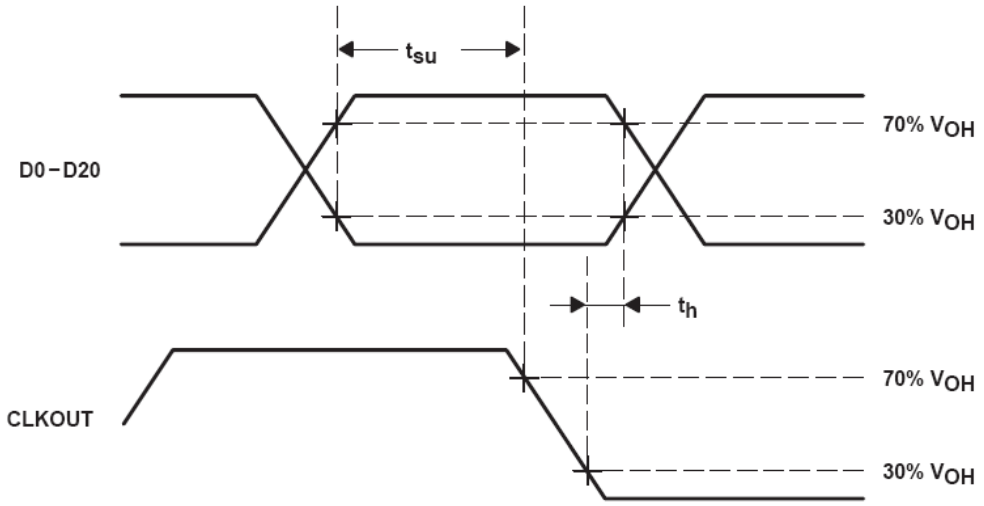
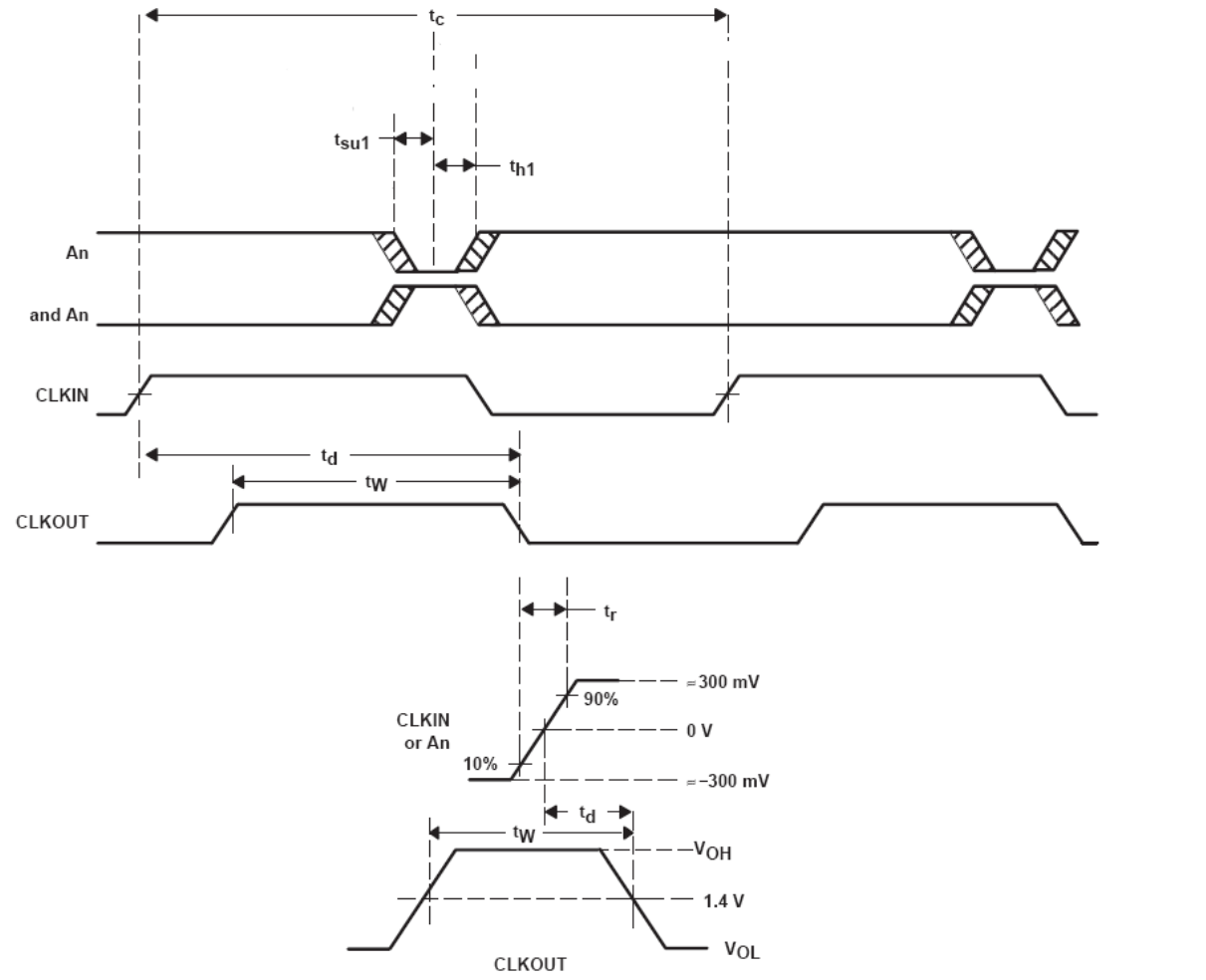


Figure 5. Setup and Hold Time Waveforms



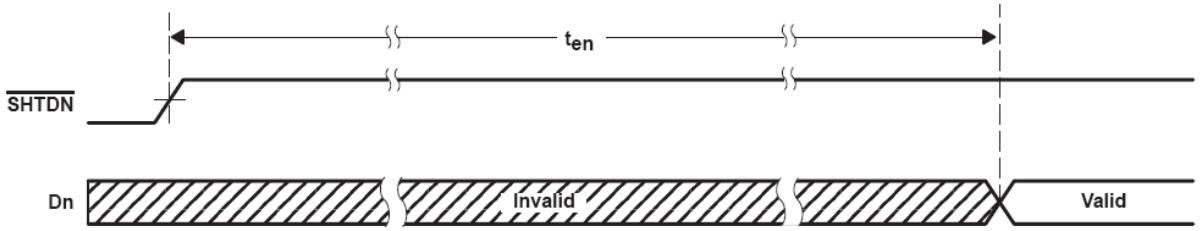


Figure 7. Enable Time Waveforms

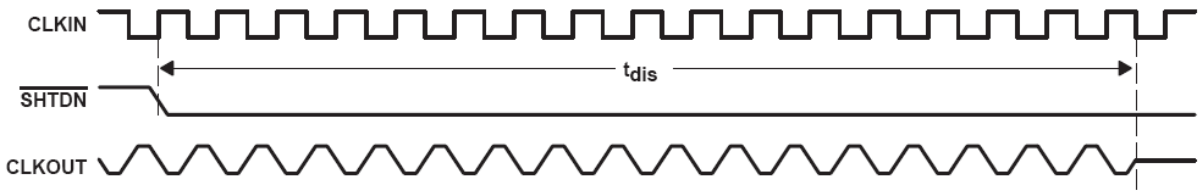
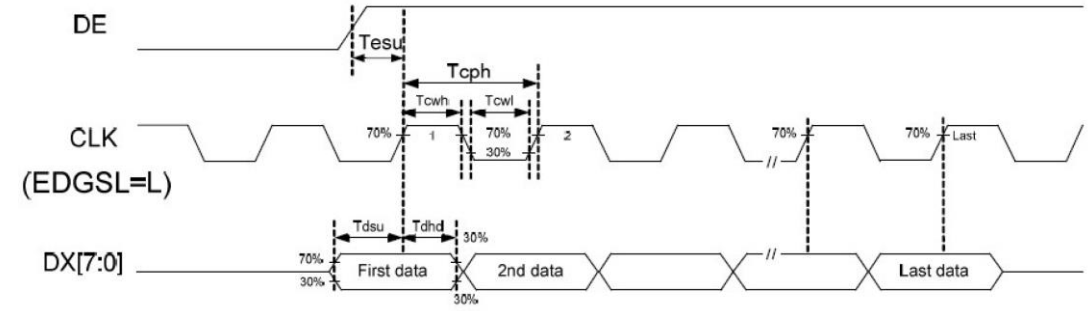


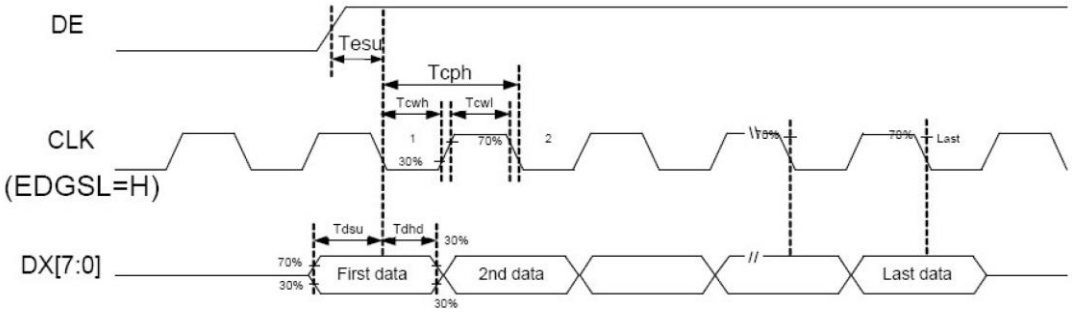
Figure 8. Disable Time Waveforms

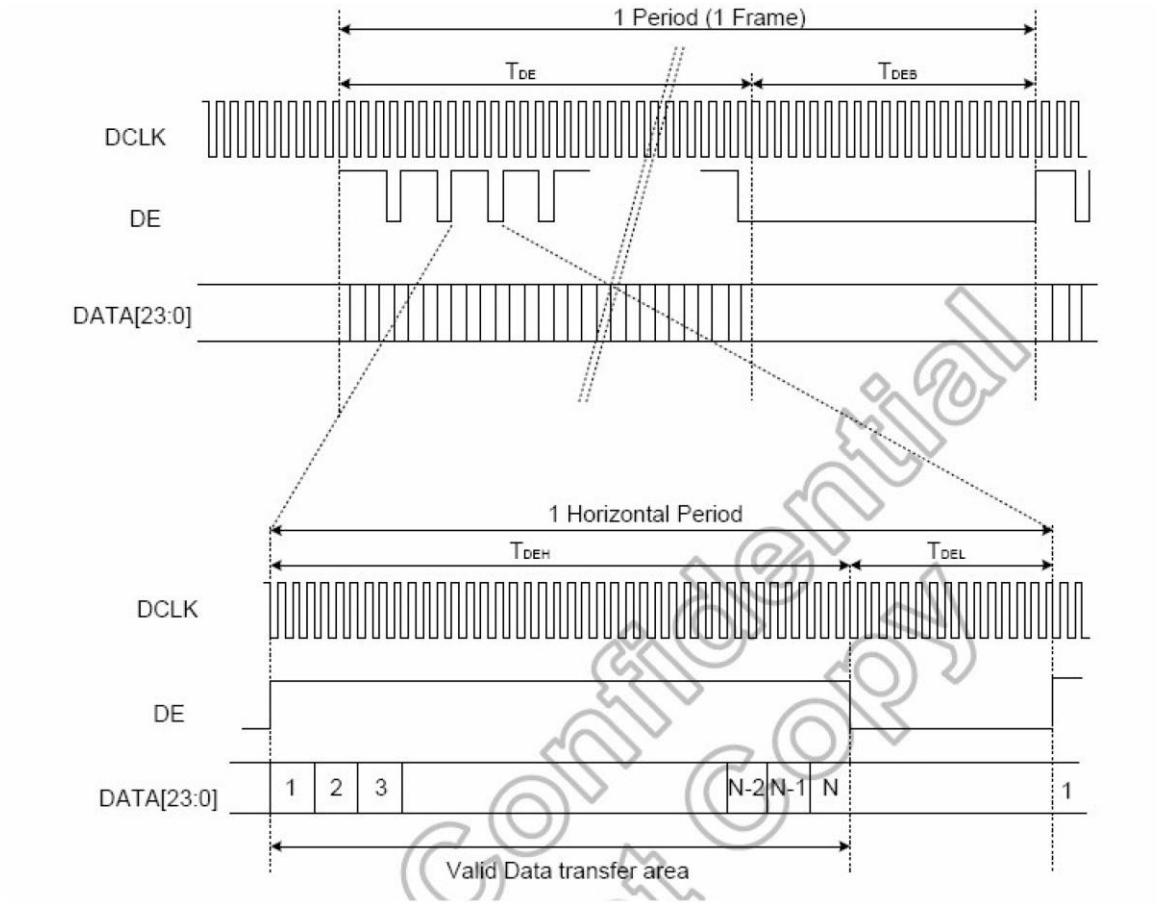
8-2 TTL RGB Timing (DE ONLY MODE)

Parameter	Symbol	Rating			Unit
		Min.	Typ.	Max.	
Data setup time	Tdsu	6	-	-	ns
Data hold time	Tdhd	6	-	-	ns
DE setup time	Tesu	6	-	-	ns
CLK frequency	F_{CPH}	29.40	33.26	42.48	MHz
CLK period	T_{CPH}	23.54	30.06	34.01	ns
CLK pulse duty	T_{CWH}	40	50	60	%
CLK pulse duty	T_{CWL}	40	50	60	%
DE period	$T_{DEH}+T_{DEL}$	1000	1056	1200	T_{CPH}
DE pulse width	T_{DEH}	-	800	-	T_{CPH}
DE frame blanking	T_{DEB}	10	45	110	$T_{DEH}+T_{DEL}$
DE frame width	T_{DE}	-	480	-	$T_{DEH}+T_{DEL}$

Note：We suggest using the typical value, so it can have better performance.

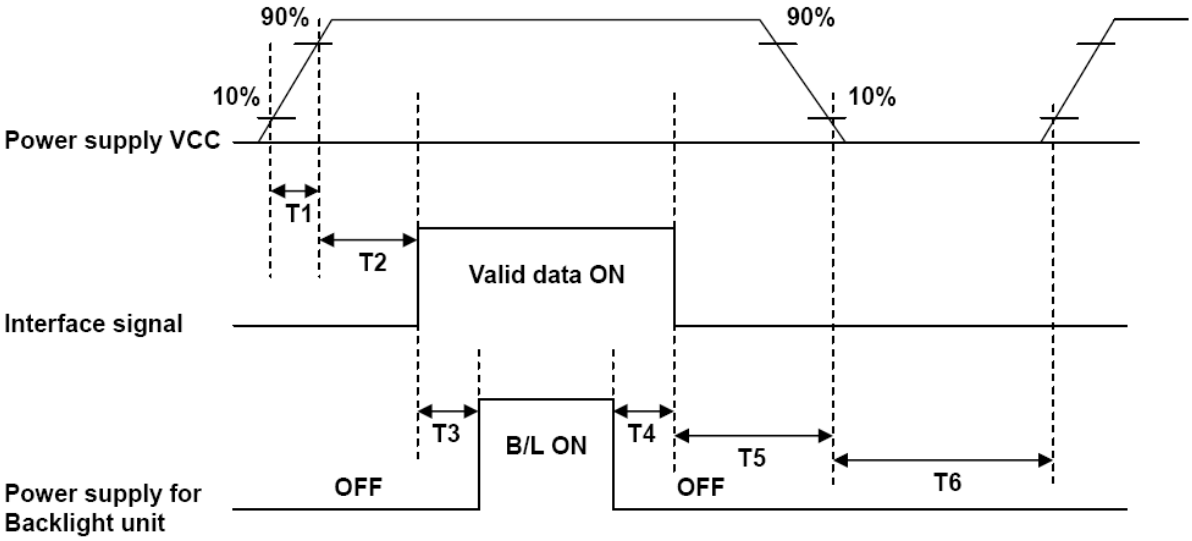






Confidential Copy

8-3 Power Sequence



Parameter	SPEC.			Unit
	Min.	Typ.	Max.	
T1	1		2	ms
T2	0	60		ms
T3	200			ms
T4	200			ms
T5	1			ms
T6	1000			ms

9. DISPLAYED COLOR AND INPUT DATA

	Color & Gray Scale	DATA SIGNAL																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

10. RELIABILITY TEST CONDITIONS

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , Dry t=240 hrs	
Low Temperature Operation	-20±3°C, Dry t=240 hrs	
High Temperature Storage	80±3°C , Dry t=240 hrs	1,2
Low Temperature Storage	-30±3°C ,Dry t=240 hrs	1,2
Thermal Shock Test	-20°C ~ 25°C ~ 70°C 30 min. 5 min. 30 min. (1 cycle) Total 100 cycle(Dry)	1,2
Humidity Test	60 °C, Humidity 90%, 240 hrs	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note(1) Condensation of water is not permitted on the module.

Note(2) The module should be inspired after 1 hour storage in normal conditions (15~35℃, 45~65%RH).

Note(3) The module shouldn't be tested over one condition, and all the tests are independent.

Note(4) All reliability tests should be done without the protective film.

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of initial value.

11. Use Precautions

11.1 Handling precautions

(1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.

(2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.

(3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.

(4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

11.2 Installing precautions

(1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.

(2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.

(3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.

(4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

11.3 Storage precautions

(1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.

(2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.

(3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

11.4 Operating precautions

(1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.

(2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.

(3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.

(4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.

(5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.

(6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.

(7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.

(8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

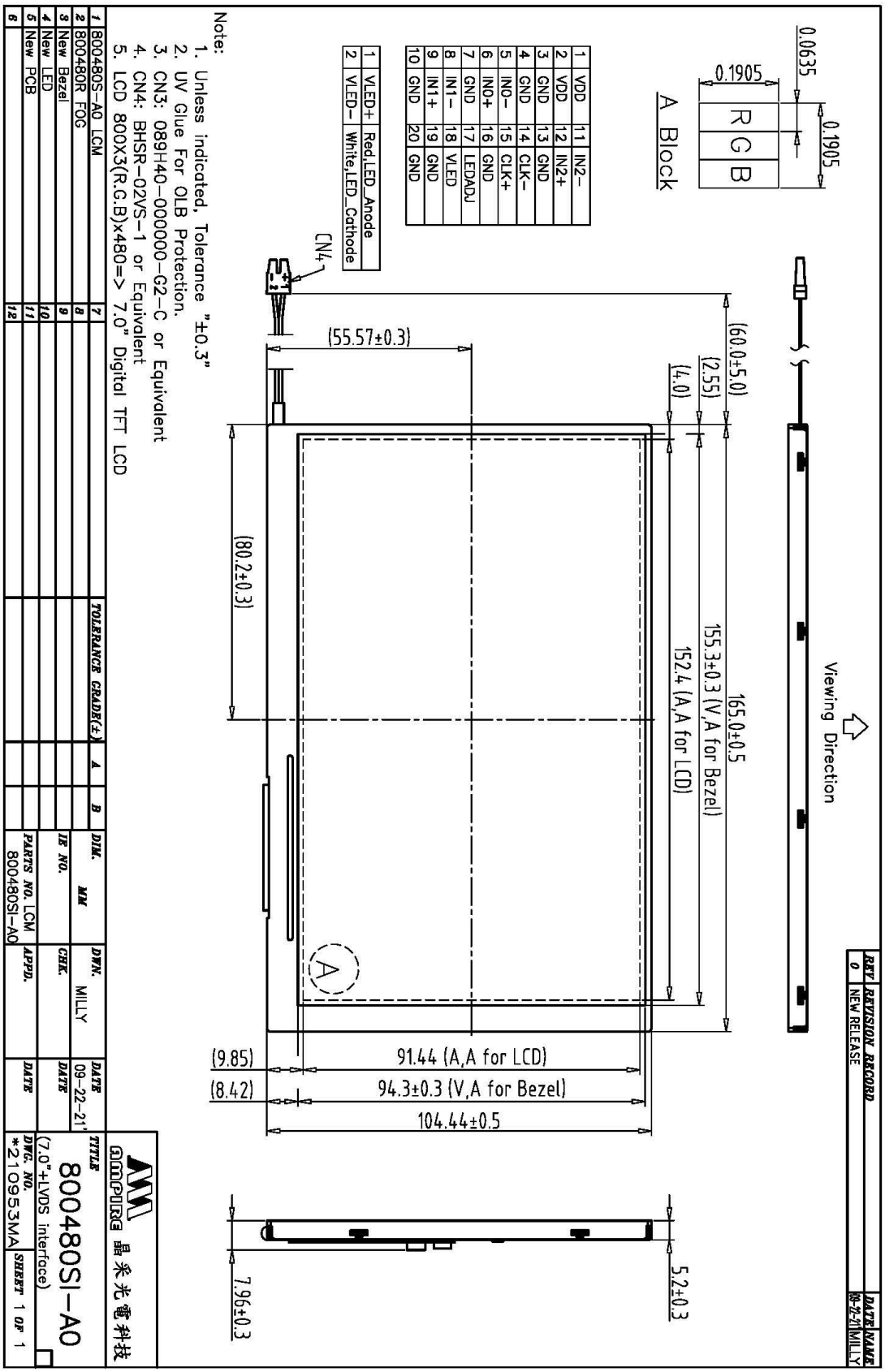
11.5 Other

(1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.

(2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.

(3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.

12. OUTLINE DIMENSION



1	VDD	IN2-
2	VDD	IN2+
3	GND	GND
4	GND	CLK-
5	IN0-	CLK+
6	IN0+	GND
7	GND	LEDADJ
8	IN1-	VLED
9	IN1+	GND
10	GND	GND

1	VLED+	Red,LED_Anode
2	VLED-	White,LED_Cathode

REV	REVISION RECORD	DATE	NAME
0	NEW RELEASE	09-22-21	MILLY

1	800480S-A0 LCM	7			TOLERANCE GRADE(±)	A	B	DIM.	MM
2	800480R FOG	8						IE NO.	
3	New Bezel	9							
4	New LED	10							
5	New PCB	11						PARTS NO.	LCM 800480SI-A0
6		12							

	DWN.	MILLY
	CHK.	
	APPD.	
	DATE	09-22-21
	DATE	
	DATE	

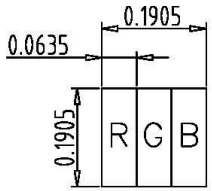
AMPIRE 晶采光電科技

TITLE: 800480SI-A0

DWG. NO.: *210953MA

SHEET 1 OF 1

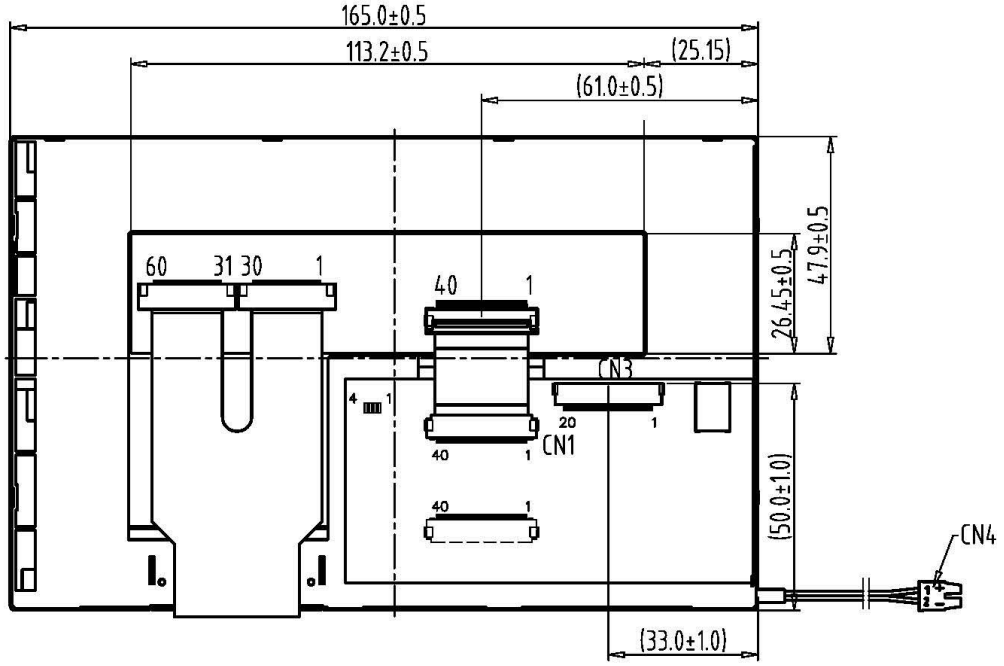
REV	REVISION RECORD	DATE	NAME
0	NEW RELEASE	09-22-21	MILLY



A Block

1	VDD	11	IN2−
2	VDD	12	IN2+
3	GND	13	GND
4	GND	14	CLK−
5	IN0−	15	CLK+
6	IN0+	16	GND
7	GND	17	LEDADJ
8	IN1−	18	VLED
9	IN1+	19	GND
10	GND	20	GND

1	VLED+	Red,LED_Anode
2	VLED−	White,LED_Cathode



Back View

Note:
1. Unless indicated, Tolerance "±0.3"
2. UV Glue For OLB Protection.
3. CN1:BHSR−02VS−1 or Equivalent
4. CN2:UJU PF050−40ZSG−F09−S or Equivalent
5. CN3:DF19G−20P−1H or equivalent
6. LCD 800X3(R.G.B)x480=> 7.0" Digital TFT LCD

				TOLERANCE GRADE(±)	A	B	DIM. MM	DWN. MILLY	DATE 09−22−21	TITLE 800480SI−A0 (7.0"+LVDS interface)
1	800480S−A0 LCM	7								
2	800480R FOG	8					IE NO.	CHK.	DATE	
3	New Bezel	9								
4	New LED	10								
5	New PCB	11					PARTS NO. LCM−1 800480SI−A0	APPD.	DATE	DWG. NO. *210954MA SHEET 1 OF 1
6		12								

AMPIRE 晶采光電科技